

## Welcome to CoWare's University Program

CoWare offers accredited universities worldwide the opportunity to gain hands-on experience in electronic system-level design (ESL) using CoWare's entire suite of software tools including **Platform Architect**, **Processor Designer** and **Signal Processing Designer** (formerly known as SPW). Through the University Program, CoWare grants classroom, as well as research licenses, giving students and professors access to state-of-the-art tools for ESL design.

For platform-driven ESL design, CoWare<sup>®</sup> Platform Architect is the SystemC-based graphical environment for capturing the entire product platform and the dashboard for initiating the platform analysis functions. Platform Architect speeds the concurrent design of system-on-chip (SoC) architectures with embedded software, enabling users to rapidly create and validate SoC designs at the transaction level in SystemC. Features include support for SystemC transaction-level platform creation, architecture analysis, simulation, and debugging, plus optional integration with third-party RTL implementation and verification flows.

CoWare<sup>®</sup> Processor Designer dramatically accelerates the design of both application-specific processors and configurable accelerators through automated software development tools, RTL and instruction set simulator generation from a single, high-level specification. These application-specific processors and configurable accelerators are increasingly essential to convergent SoC functionality. Processor Designer is used to develop a wide range of processor architectures, including architectures with DSP-specific and RISC-specific features as well as SIMD and very long instruction word (VLIW) processors.

CoWare<sup>®</sup> Signal Processing Designer (formerly known as "SPW") accelerates the design of complex, digital signal processing (DSP) systems. It is a C-based modeling and simulation environment that facilitates structured modeling and model reuse across design teams. Its efficient creation of complex DSP system models and extremely fast simulation makes Signal Processing Designer the premier choice for today's complex, multi-standard designs in the wireless and multimedia markets. It is tightly integrated with the CoWare Platform Architect and CoWare Processor Designer products supporting the CoWare Platform-driven ESL Design solution.

CoWare works closely with universities and colleges worldwide to provide state-of-the-art ESL design software to classrooms and laboratories.

### Requirements

#### **Step 1:**

Initial Application – Upon request CoWare will provide an application which must be completed and submitted for consideration. Upon approval, the university shall then need to enter into a License Agreement.

#### **Step 2:**

License Agreement – University must enter into a license agreement agreeing to use CoWare's software only for the purposes outlined in the agreement.

#### **Step 3:**

Year End Report--prior to annual license renewal period, university shall submit a report which shall include the following, if applicable:

1. Copy of published article regarding the project where CoWare tools were involved (or link to article posted on-line).
2. Application notes if available.
3. Feedback on tools or documentation.
4. A summary of results.

**License Fees**

In most cases, CoWare does not charge License fees to universities. Depending on geographical location and other factors, a small Administration fee may be applied and it shall be quoted at time of application.

Please send inquiries about CoWare's University Program to [UniversityProgram@CoWare.com](mailto:UniversityProgram@CoWare.com).



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