

# CoWare® Model Library IP

*A Library of SystemC-based Models for Platform-driven ESL Design*

## Processor Support Packages (PSP):

### ARM

- ARM7 Family
  - ARM9 Family
  - ARM10 Family
  - ARM11 Family
  - ARM11 MPCore
  - ARM Cortex Family
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### IBM

- IBM PowerPC 405/440 Family
  - IBM PowerPC750GX/GL Family
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### MIPS TECHNOLOGIES

- MIPS 4KE Family
  - MIPS 24K Family
  - MIPS 34K Family
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### tensilica

- Tensilica Xtensa Family
  - Tensilica Diamond Core Family
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### VeriSilicon

- Verisilicon ZSP 400 Family
  - Verisilicon ZSP 500 Family
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### CEVA™ The DSP Powerhouse

- CEVA TeakLite/TeakLite II Family
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### MeP Media embedded Processor

- Toshiba MeP Family
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## Bus Libraries (BL):

### ARM

- ARM AMBA 2.0
  - ARM AMBA 3 AXI
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### SONICS™ smart interconnect IP

- Sonics SMX
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### IBM

- IBM CoreConnect
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## OTHERS

- Architects View FrameWork
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## Memory Subsystem Models:

### SONICS™ smart interconnect IP

- Sonics MemMax
  - Sonics MemDDR
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## OTHERS

- AHB/APB PrimeCell Library
  - AXI PrimeCell Library
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The ESL Design Leader

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